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serving nearly forty years, becoming a distinguished engineer at Google. He currently is Vice Chair of the Board of Directors of the RISC-V Foundation, and the Pardee Professor of Computer Science, Emeritus at UC Berkeley. Patterson is noted for his pioneering contributions to red

David Patterson (computer scientist) - Wikipedia

David A. Patterson (University of California at Berkeley) has taught computer architecture since joining the faculty in 1977, and is holder of the E.H. and M.E. Pardee Chair of Computer Science. At Berkeley, he led the design and implementation of RISC I, likely the first VLSI Reduced Instruction Set Computer.

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Real-Time Intelligent Secure Explainable Systems (RISELab) Biography. David Patterson is the Pardee Professor of Computer Science, Emeritus at the University of California at Berkeley, which he joined after graduating from UCLA in 1976. Dave's research style is to identify critical questions for the IT industry and gather inter-disciplinary groups of faculty and graduate students to answer them.

David A. Patterson | EECS at UC Berkeley

David Patterson is a Turing award winner and professor of computer science at Berkeley. He is known for pioneering

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Patterson was hired into the University
of California at Berkeley's computer
science/ electrical engineering
department upon graduation.

Patterson's PhD thesis was on writable
control store methods for operating
systems, so he began his Berkeley
career with Carlo Sequin working on the
X-TREE project led by Alvin Despain.
Years later, he called this modular
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